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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND classify) AND "binary search"): 3 patents.

Hits 1 through 3 out of 3

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packet AND classify and "binary search"

PAT. NO. Title

- 1 [6,041,053](#) Technique for efficiently classifying packets using a trie-indexed hierarchy forest that accommodates wildcards
- 2 [5,995,971](#) Apparatus and accompanying methods, using a trie-indexed hierarchy forest, for storing wildcard-based patterns and, given an input key, retrieving, from the forest, a stored pattern that is identical to or more general than the key
- 3 [4,987,539](#) Apparatus and method for multidimensional characterization of objects in real time

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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND "binary search"): 18 patents.

Hits 1 through 18 out of 18

[Jump To](#)[Refine Search](#)

packet AND pipeline and "binary search"

PAT. NO. Title

- 1 [6,122,275](#) Real-time processing for virtual circuits in packet switching
- 2 [6,052,683](#) Address lookup in packet data communication networks
- 3 [5,978,951](#) High speed cache management unit for use in a bridge/router
- 4 [5,951,651](#) Packet filter system using BITMAP vector of filter rules for routing packet through network
- 5 [5,920,886](#) Accelerated hierarchical address filtering and translation using binary and ternary CAMs
- 6 [5,825,774](#) Packet characterization using code vectors
- 7 [5,802,054](#) Atomic network switch with integrated circuit switch nodes
- 8 [5,748,633](#) Method and apparatus for the concurrent reception and transmission of packets in a communications internetworking device
- 9 [5,651,002](#) Internetworking device with enhanced packet header translation and memory
- 10 [5,490,252](#) System having central processor for transmitting generic packets to another processor to be altered and transmitting altered packets back to central processor for routing
- 11 [5,455,825](#) Tag-based scheduling system for digital communication switch
- 12 [5,400,331](#) Communication network interface with screeners for incoming messages
- 13 [4,949,391](#) Adaptive image acquisition system
- 14 [4,882,629](#) Adaptive exposure control system
- 15 [4,525,780](#) Data processing system having a memory using object-based information and a protection scheme for determining access rights to such information
- 16 [4,493,027](#) Method of performing a call operation in a digital data processing system having microcode call and return operations
- 17 [4,455,602](#) Digital data processing system having an I/O means using unique address providing and access priority control techniques
- 18 [4,445,177](#) Digital data processing system utilizing a unique arithmetic logic unit for handling

18 4,445,177 Digital data processing system utilizing a unique arithmetic logic unit for handling
uniquely identifiable addresses for operands and instructions





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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND "binary search") AND breakpoints): 2 patents.

Hits 1 through 2 out of 2

[Jump To](#)[Refine Search](#)

packet AND pipeline AND "binary search" AND breakpoints

PAT. NO. Title

1 [4,949,391](#) Adaptive image acquisition system

2 [4,882,629](#) Adaptive exposure control system

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Searching 1976-2000...

Results of Search in 1976-2000 db for:
("packet classifier" AND dimensional): 0 patents.

No patents have matched your query

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"packet classifier" and dimensional



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Searching 1976-2000...

Results of Search in 1976-2000 db for:
("packet classifier" AND dimension): 0 patents.

No patents have matched your query

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"packet classifier" and dimension



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Searching 1976-2000...

Results of Search in 1976-2000 db for:
("packet classifier" AND pipeline): 0 patents.

No patents have matched your query

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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND classifier) AND pipeline): 6.patents.

Hits 1 through 6 out of 6

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packet and classifier and pipeline

PAT. NO. Title

1 [6,057,847](#) System and method of image generation and encoding using primitive reprojection

? 2 [5,781,549](#) Method and apparatus for switching data packets in a data network

3 [5,644,386](#) Visual recognition system for LADAR sensors

? 4 [5,509,006](#) Apparatus and method for switching packets using tree memory

5 [5,341,142](#) Target acquisition and tracking system

6 [4,649,498](#) Computer systems for curve-solid classification and solid modeling

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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND sniffer): 0 patents.

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packet and pipeline and header and sniffer



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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND detector): 132 patents.

Hits 1 through 50 out of 132

[Next 50 Hits](#)[Jump To](#)[Refine Search](#)

packet and pipeline and header and detector

PAT. NO. Title

- 1 [6,122,716 System and method for authenticating a computer memory](#)
- 2 [6,119,213 Method for addressing data having variable data width using a fixed number of bits for address and width defining fields](#)
- 3 [6,118,724 Memory controller architecture](#)
- 4 [6,112,017 Pipeline processing machine having a plurality of reconfigurable processing stages interconnected by a two-wire interface bus](#)
- 5 [6,104,751 Apparatus and method for decompressing high definition pictures](#)
- 6 [6,091,705 Method and apparatus for a fault tolerant, software transparent and high data integrity extension to a backplane bus or interconnect](#)
- 7 [6,088,355 Processing system with pointer-based ATM segmentation and reassembly](#)
- 8 [6,081,783 Dual processor digital audio decoder with shared memory data transfer and task partitioning for decompressing compressed audio data, and systems and methods using the same](#)
- 9 [6,081,570 Parallel integrated frame synchronizer chip](#)
- 10 [6,081,514 Method and apparatus for enhanced functions using a reserved field](#)
- 11 [6,079,009 Coding standard token in a system compromising a plurality of pipeline stages](#)
- 12 [6,076,121 Method of network addressing and translation](#)
- 13 [6,072,830 Method for generating a compressed video signal](#)
- 14 [6,067,417 Picture start token](#)
- 15 [6,061,749 Transformation of a first dataword received from a FIFO into an input register and subsequent dataword from the FIFO into a normalized output dataword](#)
- 16 [6,047,112 Technique for initiating processing of a data stream of encoded video information](#)

- 17 6,038,380 Data pipeline system and data encoding method
- 18 6,035,126 Data pipeline system and data encoding method
- 19 6,018,776 System for microprogrammable state machine in video parser clearing and resetting processing stages responsive to flush token generating by token generator responsive to received data
- 20 6,016,428 Registration control of mobile stations in a wireless communication system
- 21 6,011,546 Programming structure for user interfaces
- 22 5,995,727 Video decompression
- 23 5,991,817 Apparatus and method for a network router
- 24 5,991,308 Lower overhead method for data transmission using ATM and SCDMA over hybrid fiber coax cable plant
- 25 5,984,512 Method for storing video information
- 26 5,978,592 Video decompression and decoding system utilizing control and data tokens
- 27 5,974,862 Method for detecting leaks in pipelines
- 28 5,970,069 Single chip remote access processor
- 29 5,963,596 Audio decoder circuit and method of operation
- 30 5,963,154 Technique for decoding variable and fixed length codes
- 31 5,956,741 Interface for connecting a bus to a random access memory using a swing buffer and a buffer manager
- 32 5,956,519 Picture end token in a system comprising a plurality of pipeline stages
- 33 5,953,691 Processing system with graphics data prescaling
- 34 5,930,295 Mobile terminal apparatus including net radio service in a mobile satellite service communication system
- 35 5,926,647 Processing system with dynamic alteration of a color look-up table
- 36 5,926,226 Method for adjusting the quality of a video coder
- 37 5,923,385 Processing system with single-buffered display capture
- 38 5,889,949 Processing system with memory arbitrating between memory access requests in a set top box
- 39 5,881,301 Inverse modeller
- 40 5,878,273 System for microprogrammable state machine in video parser disabling portion of processing stages responsive to sequence sub-- end token generating by token generator responsive to received data
- 41 5,877,812 Method and apparatus for increasing channel utilization for digital video transmission
- 42 5,864,681 Video encoder/decoder system
- 43 5,862,136 Telecommunications apparatus and method
- 44 5,852,601 Method and apparatus for reactive congestion control in an asynchronous transfer mode (ATM) network
- 45 5,842,033 Padding apparatus for passing an arbitrary number of bits through a buffer in a pipeline system
- 46 5,841,771 Telecommunications switch apparatus and method for time switching

- 47 [5,835,792 Token-based adaptive video processing arrangement](#)
- 48 [5,835,740 Data pipeline system and data encoding method](#)
- 49 [5,832,224 Entity management system](#)
- 50 [5,831,678 Video encoder/decoder system](#)

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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND detector): 132 patents.

Hits 51 through 100 out of 132

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packet and pipeline and header and detector

PAT. NO. Title

- 51 [5,829,007](#) Technique for implementing a swing buffer in a memory array
- 52 [5,828,907](#) Token-based adaptive video processing arrangement
- 53 [5,828,899](#) System for peripheral devices recursively generating unique addresses based on the number of devices connected dependent upon the relative position to the port
- 54 [5,822,512](#) Switching control in a fault tolerant system
- 55 [5,821,885](#) Video decompression
- 56 [5,809,543](#) Fault tolerant extended processing complex for redundant nonvolatile file caching
- 57 [5,809,270](#) Inverse quantizer
- 58 [5,805,933](#) Image processing apparatus, image processing method and network system
- 59 [5,805,914](#) Data pipeline system and data encoding method
- 60 [5,805,228](#) Video encoder/decoder system
- 61 [5,801,973](#) Video decompression
- 62 [5,801,785](#) Method and system for processing two analog composite video signals
- 63 [5,798,719](#) Parallel Huffman decoder
- 64 [5,794,181](#) Method for processing a subband encoded audio data stream
- 65 [5,793,427](#) Processing system with delta-based video data encoding
- 66 [5,790,842](#) Processing system with simultaneous utilization of multiple clock signals
- 67 [5,784,631](#) Huffman decoder
- 68 [5,768,629](#) Token-based adaptive video processing arrangement

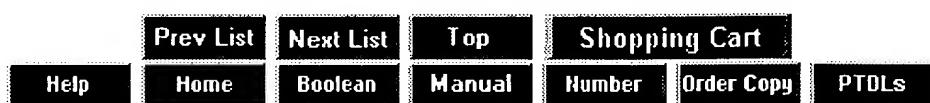
69 [5,768,561 Tokens-based adaptive video processing arrangement](#)
70 [5,765,197 Method and system for authentication of a memory unit for a computer system](#)
71 [5,761,698 Computer system having audio/video/CD drive controller/coprocessor having integral memory interface, graphics coprocessor, digital signal processor, compact disk controller, and video controller](#)
72 [5,761,343 Image reproduction apparatus and image reproduction method](#)
73 [5,761,259 Apparatus, method and article of manufacture for carrier frequency compensation in a FM radio](#)
74 [5,751,114 Apparatus, method and article of manufacture for carrier frequency compensation in a FM radio transmitter](#)
75 [5,745,837 Apparatus and method for digital data transmission over a CATV system using an ATM transport protocol and SCDMA](#)
76 [5,740,460 Arrangement for processing packetized data](#)
77 [5,737,028 Previous channel listing with cursor controlled user interface for television video displays](#)
78 [5,734,676 Apparatus, method and article of manufacture for carrier frequency compensation in a FM radio receiver](#)
79 [5,729,556 System decoder circuit with temporary bit storage and method of operation](#)
80 [5,724,537 Interface for connecting a bus to a random access memory using a two wire link](#)
81 [5,703,793 Video decompression](#)
82 [5,666,516 Protected programmable memory cartridge having selective access circuitry](#)
83 [5,664,223 System for independently transferring data using two independently controlled DMA engines coupled between a FIFO buffer and two separate buses respectively](#)
84 [5,657,454 Audio decoder circuit and method of operation](#)
85 [5,657,423 Hardware filter circuit and address circuitry for MPEG encoded data](#)
86 [5,657,091 Video display controller, user interface and programming structure for such interface](#)
87 [5,652,630 Video receiver display, three axis remote control, and microcontroller for executing programs](#)
88 [5,649,119 Data queuing apparatus](#)
89 [5,648,987 Rapid-update adaptive channel-equalization filtering for digital radio receivers, such as HDTV receivers](#)
90 [5,644,310 Integrated audio decoder system and method of operation](#)
91 [5,642,437 System decoder circuit with temporary bit storage and method of operation](#)
92 [5,631,848 System decoder circuit and method of operation](#)
93 [5,619,499 Protocol processor in communication network transferring data in asynchronous transfer mode](#)
94 [5,608,907 Extensible entity management system including an information manager for obtaining, storing and retrieving information from entities](#)
95 [5,606,374 Video receiver display of menu overlaying video](#)
96 [5,604,544 Video receiver display of cursor overlaying video](#)

97 [5,603,012 Start code detector](#)

98 [5,602,597 Video receiver display of video overlaying menu](#)

99 [5,598,388 Storing plural data records on tape in an entity with an index entry common to those records](#)

100 [5,594,490 System for distributing video/audio files from central location to a plurality of cable headends](#)





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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND detector): 132 patents.

Hits 101 through 132 out of 132

[Prev. 50 Hits](#)[Jump To](#)[Refine Search](#)

packet and pipeline and header and detector

PAT. NO. Title

101 [5,579,052](#) Picture processing system

102 [5,557,796](#) Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands

103 [5,553,095](#) Method and apparatus for exchanging different classes of data during different time intervals

104 [5,548,340](#) Intelligent television receivers combinations including video displays, and methods for diversion of television viewers by visual image modification

105 [5,539,479](#) Video receiver display of cursor and menu overlaying video

106 [5,539,478](#) Video receiver display and three axis remote control

107 [5,537,549](#) Communication network with time coordinated station activity by time slot and periodic interval number

108 [5,524,281](#) Apparatus and method for measuring the phase and magnitude of microwave signals

109 [5,500,863](#) CRC operational system

110 [5,493,571](#) Apparatus and method for digital communications with improved delimiter detection

111 [5,475,838](#) Extensible entity management system including rule-based alarms

112 [5,471,488](#) Clock fault detection circuit

113 [5,467,211](#) Optoelectric sliding banyan network

114 [5,457,687](#) Method and apparatus for backward explicit congestion notification (BECN) in an ATM network

115 [5,448,558](#) Method and apparatus for managing packet FIFOs

116 [5,436,899](#) High performance digitally multiplexed transmission system

117 [5,400,331](#) Communication network interface with screeners for incoming messages

118 [5,398,241](#) High speed asynchronous multiplexer demultiplexer

119 [5,345,587](#) Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands

120 [5,341,142](#) Target acquisition and tracking system

121 [5,313,454](#) Congestion control for cell networks

122 [5,298,895](#) Data compression method and apparatus utilizing an adaptive dictionary

123 [5,280,600](#) Storage of compressed data with algorithm

124 [5,138,615](#) Reconfiguration system and method for high-speed mesh connected local area network

125 [5,088,091](#) High-speed mesh connected local area network

126 [4,977,582](#) Synchronization of non-continuous digital bit streams

127 [4,748,617](#) Very high-speed digital data bus

128 [4,525,780](#) Data processing system having a memory using object-based information and a protection scheme for determining access rights to such information

129 [4,493,027](#) Method of performing a call operation in a digital data processing system having microcode call and return operations

130 [4,455,602](#) Digital data processing system having an I/O means using unique address providing and access priority control techniques

131 [4,445,177](#) Digital data processing system utilizing a unique arithmetic logic unit for handling uniquely identifiable addresses for operands and instructions

132 [4,438,511](#) Packetized ensemble modem

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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND detector): 132 patents.

Hits 101 through 132 out of 132

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PAT. NO. Title

- 101 [5,579,052 Picture processing system](#)
- 102 [5,557,796 Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands](#)
- 103 [5,553,095 Method and apparatus for exchanging different classes of data during different time intervals](#)
- 104 [5,548,340 Intelligent television receivers combinations including video displays, and methods for diversion of television viewers by visual image modification](#)
- 105 [5,539,479 Video receiver display of cursor and menu overlaying video](#)
- 106 [5,539,478 Video receiver display and three axis remote control](#)
- 107 [5,537,549 Communication network with time coordinated station activity by time slot and periodic interval number](#)
- 108 [5,524,281 Apparatus and method for measuring the phase and magnitude of microwave signals](#)
- 109 [5,500,863 CRC operational system](#)
- 110 [5,493,571 Apparatus and method for digital communications with improved delimiter detection](#)
- 111 [5,475,838 Extensible entity management system including rule-based alarms](#)
- 112 [5,471,488 Clock fault detection circuit](#)
- 113 [5,467,211 Optoelectric sliding banyan network](#)
- 114 [5,457,687 Method and apparatus for backward explicit congestion notification \(BECN\) in an ATM network](#)
- 115 [5,448,558 Method and apparatus for managing packet FIFOs](#)
- 116 [5,436,899 High performance digitally multiplexed transmission system](#)
- 117 [5,400,331 Communication network interface with screeners for incoming messages](#)

118 [5,398,241](#) High speed asynchronous multiplexer demultiplexer
119 [5,345,587](#) Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands
120 [5,341,142](#) Target acquisition and tracking system
121 [5,313,454](#) Congestion control for cell networks
122 [5,298,895](#) Data compression method and apparatus utilizing an adaptive dictionary
123 [5,280,600](#) Storage of compressed data with algorithm
124 [5,138,615](#) Reconfiguration system and method for high-speed mesh connected local area network
125 [5,088,091](#) High-speed mesh connected local area network
126 [4,977,582](#) Synchronization of non-continuous digital bit streams
127 [4,748,617](#) Very high-speed digital data bus
128 [4,525,780](#) Data processing system having a memory using object-based information and a protection scheme for determining access rights to such information
129 [4,493,027](#) Method of performing a call operation in a digital data processing system having microcode call and return operations
130 [4,455,602](#) Digital data processing system having an I/O means using unique address providing and access priority control techniques
131 [4,445,177](#) Digital data processing system utilizing a unique arithmetic logic unit for handling uniquely identifiable addresses for operands and instructions
132 [4,438,511](#) Packetized ensemble modem





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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND categories: 82 patents.

Hits 1 through 50 out of 82

Final 32 Hits

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packet and pipeline and header and categories

PAT. NO. Title

- 1 [6,112,017 Pipeline processing machine having a plurality of reconfigurable processing stages interconnected by a two-wire interface bus](#)
- 2 [6,088,753 Bus arrangements for interconnection of discrete and/or integrated modules in a digital system and associated method](#)
- 3 [6,084,864 Traffic routing for satellite communication system](#)
- 4 [6,081,665 Method for efficient soft real-time execution of portable byte code computer programs](#)
- 5 [6,079,009 Coding standard token in a system compromising a plurality of pipeline stages](#)
- 6 [6,076,121 Method of network addressing and translation](#)
- 7 [6,072,830 Method for generating a compressed video signal](#)
- 8 [6,067,417 Picture start token](#)
- 9 [6,061,665 System, method and article of manufacture for dynamic negotiation of a network payment framework](#)
- 10 [6,047,112 Technique for initiating processing of a data stream of encoded video information](#)
- 11 [6,038,380 Data pipeline system and data encoding method](#)
- 12 [6,035,126 Data pipeline system and data encoding method](#)
- 13 [6,018,776 System for microprogrammable state machine in video parser clearing and resetting processing stages responsive to flush token generating by token generator responsive to received data](#)
- 14 [5,999,518 Distributed telecommunications switching system and method](#)
- 15 [5,983,303 Bus arrangements for interconnection of discrete and/or integrated modules in a digital system and associated method](#)
- 16 [5,978,592 Video decompression and decoding system utilizing control and data tokens](#)

- 17 5,956,519 Picture end token in a system comprising a plurality of pipeline stages
- 18 5,953,318 Distributed telecommunications switching system and method
- 19 5,930,254 Non-blocking dynamic fast packet switch for satellite communication system
- 20 5,926,459 Rate shaping in per-flow queued routing mechanisms for available bit rate service
- 21 5,926,226 Method for adjusting the quality of a video coder
- 22 5,925,097 Directly programmable distribution element
- 23 5,909,638 High speed video distribution and manufacturing system
- 24 5,883,901 Communications system including synchronization information for timing upstream transmission of data and ability to vary slot duration
- 25 5,881,301 Inverse modeller
- 26 5,864,681 Video encoder/decoder system
- 27 5,842,033 Padding apparatus for passing an arbitrary number of bits through a buffer in a pipeline system
- 28 5,835,740 Data pipeline system and data encoding method
- 29 5,832,224 Entity management system
- 30 5,831,678 Video encoder/decoder system
- 31 5,809,270 Inverse quantizer
- 32 5,805,914 Data pipeline system and data encoding method
- 33 5,805,821 Video optimized media streamer user interface employing non-blocking switching to achieve isochronous data transfers
- 34 5,805,228 Video encoder/decoder system
- 35 5,796,715 Non-blocking dynamic fast packet switch for satellite communication system
- 36 5,787,483 High-speed data communications modem
- 37 5,784,631 Huffman decoder
- 38 5,784,597 Communications network system including acknowledgement indicating successful receipt of request for reserved communication slots and start time for said reserved communication slots
- 39 5,768,561 Tokens-based adaptive video processing arrangement
- 40 5,765,037 System for executing instructions with delayed firing times
- 41 5,761,417 Video data streamer having scheduler for scheduling read request for individual data buffers associated with output ports of communication node to one storage node
- 42 5,740,164 Traffic routing for satellite communication system
- 43 5,717,394 Method and apparatus for encoding and decoding data
- 44 5,712,976 Video data streamer for simultaneously conveying same one or different ones of data blocks stored in storage node to each of plurality of communication nodes
- 45 5,668,948 Media streamer with control node enabling same isochronous streams to appear simultaneously at output ports or different streams to appear simultaneously at output ports
- 46 5,634,004 Directly programmable distribution element
- 47 5,608,907 Extensible entity management system including an information manager for obtaining,

storing and retrieving information from entities

48 5,608,662 Packet filter engine

49 5,603,058 Video optimized media streamer having communication nodes received digital data from storage node and transmitted said data to adapters for generating isochronous digital data streams

50 5,603,012 Start code detector

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: 82 patents.

Hits 51 through 82 out of 82[Prev. 50 Hits](#)[Jump To](#)[Refine Search](#)

packet

PAT. NO. Title

- 51 [5,594,869 Method and apparatus for end-to-end encryption of a data packet in a computer network](#)
- 52 [5,592,622 Network intermediate system with message passing architecture](#)
- 53 [5,586,264 Video optimized media streamer with cache management](#)
- 54 [5,557,796 Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands](#)
- 55 [5,517,628 Computer with instructions that use an address field to select among multiple condition code registers](#)
- 56 [5,475,838 Extensible entity management system including rule-based alarms](#)
- 57 [5,463,624 Bus arbitration method for telecommunications switching](#)
- 58 [5,345,587 Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands](#)
- 59 [5,341,142 Target acquisition and tracking system](#)
- 60 [5,235,644 Probabilistic cryptographic processing method](#)
- 61 [5,233,348 Variable length code word decoder for use in digital communication systems](#)
- 62 [5,195,089 Apparatus and method for a synchronous, high speed, packet-switched bus](#)
- 63 [5,163,131 Parallel I/O network file server architecture](#)
- 64 [5,161,193 Pipelined cryptography processor and method for its use in communication networks](#)
- 65 [5,113,523 High performance computer system](#)
- 66 [5,099,517 Frame status encoding for communication networks](#)
- 67 [5,086,469 Encryption with selective disclosure of protocol identifiers](#)
- 68 [5,070,528 Generic encryption technique for communication networks](#)

69 [5,021,945](#) Parallel processor system for processing natural concurrencies and method therefor
70 [4,991,133](#) Specialized communications processor for layered protocols
71 [4,967,375](#) Fast architecture for graphics processor
72 [4,922,414](#) Symbolic language data processing system
73 [4,887,235](#) Symbolic language data processing system
74 [4,881,230](#) Expert system for processing errors in a multiplex communications system
75 [4,873,687](#) Failing resource manager in a multiplex communication system
76 [4,847,755](#) Parallel processing method and apparatus for increasing processing throughout by parallel processing low level instructions having natural concurrencies
77 [4,817,092](#) Threshold alarms for processing errors in a multiplex communications system
78 [4,748,617](#) Very high-speed digital data bus
79 [4,525,780](#) Data processing system having a memory using object-based information and a protection scheme for determining access rights to such information
80 [4,493,027](#) Method of performing a call operation in a digital data processing system having microcode call and return operations
81 [4,455,602](#) Digital data processing system having an I/O means using unique address providing and access priority control techniques
82 [4,445,177](#) Digital data processing system utilizing a unique arithmetic logic unit for handling uniquely identifiable addresses for operands and instructions

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Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND breakpoint): 15 patents.

Hits 1 through 15 out of 15

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packet and pipeline and header and breakpoint

PAT. NO. Title

- 1 [6,081,783](#) Dual processor digital audio decoder with shared memory data transfer and task partitioning for decompressing compressed audio data, and systems and methods using the same
- 2 [6,046,752](#) Peer-to-peer parallel processing graphics accelerator
- 3 [5,917,502](#) Peer-to-peer parallel processing graphics accelerator
- 4 [5,680,568](#) Instruction format with sequentially performable operand address extension modification
- 5 [5,201,039](#) Multiple address-space data processor with addressable register and context switching
- 6 [5,182,811](#) Exception, interrupt, and trap handling apparatus which fetches addressing and context data using a single instruction following an interrupt
- 7 [5,113,523](#) High performance computer system
- 8 [5,029,069](#) Data processor
- 9 [5,021,947](#) Data-flow multiprocessor architecture with three dimensional multistage interconnection network for efficient signal and data processing
- 10 [4,922,414](#) Symbolic language data processing system
- 11 [4,887,235](#) Symbolic language data processing system
- 12 [4,760,525](#) Complex arithmetic vector processor for performing control function, scalar operation, and set-up of vector signal processing instruction
- 13 [4,525,780](#) Data processing system having a memory using object-based information and a protection scheme for determining access rights to such information
- 14 [4,493,027](#) Method of performing a call operation in a digital data processing system having microcode call and return operations
- 15 [4,455,602](#) Digital data processing system having an I/O means using unique address providing and access priority control techniques





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Searching 1976-2000...

Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND rules): 175 patents.

Hits 1 through 50 out of 175

[Next 50 Hits](#)[Jump To](#)[Refine Search](#)

packet and pipeline and header and rules

PAT. NO. Title

- 1 [6,119,215 Synchronization and control system for an arrayed processing engine](#)
- 2 [6,119,213 Method for addressing data having variable data width using a fixed number of bits for address and width defining fields](#)
- 3 [6,118,724 Memory controller architecture](#)
- 4 [6,112,017 Pipeline processing machine having a plurality of reconfigurable processing stages interconnected by a two-wire interface bus](#)
- 5 [6,105,119 Data transfer circuitry, DSP wrapper circuitry and improved processor devices, methods and systems](#)
- 6 [6,101,599 System for context switching between processing elements in a pipeline of processing elements](#)
- 7 [6,094,655 Method of creating and using notes decision capsules](#)
- 8 [6,094,575 Communication system and method](#)
- 9 [6,094,436 Integrated multiport switch having shared media access control circuitry](#)
- 10 [6,091,705 Method and apparatus for a fault tolerant, software transparent and high data integrity extension to a backplane bus or interconnect](#)
- 11 [6,088,706 System and method for managing replicated data by merging the retrieved records to generate a sequence of modifications](#)
- 12 [6,084,864 Traffic routing for satellite communication system](#)
- 13 [6,081,812 Identifying at-risk components in systems with redundant components](#)
- 14 [6,079,009 Coding standard token in a system compromising a plurality of pipeline stages](#)
- 15 [6,076,121 Method of network addressing and translation](#)
- 16 [6,076,100 Server-side chat monitor](#)

- 17 6,072,830 Method for generating a compressed video signal
- 18 6,070,149 Virtual sales personnel
- 19 6,067,417 Picture start token
- 20 6,061,749 Transformation of a first dataword received from a FIFO into an input register and subsequent dataword from the FIFO into a normalized output dataword
- 21 6,061,665 System, method and article of manufacture for dynamic negotiation of a network payment framework
- 22 6,047,112 Technique for initiating processing of a data stream of encoded video information
- 23 6,041,400 Distributed extensible processing architecture for digital signal processing applications
- 24 6,038,380 Data pipeline system and data encoding method
- 25 6,035,126 Data pipeline system and data encoding method
- 26 6,029,212 Method of handling arbitrary size message queues in which a message is written into an aligned block of external registers within a plurality of external registers
- 27 6,021,333 Method and system for transferring information within a mobile communication system
- 28 6,018,776 System for microprogrammable state machine in video parser clearing and resetting processing stages responsive to flush token generating by token generator responsive to received data
- 29 6,016,428 Registration control of mobile stations in a wireless communication system
- 30 6,000,020 Hierarchical storage management from a mirrored file system on a storage network segmented by a bridge
- 31 5,995,727 Video decompression
- 32 5,991,308 Lower overhead method for data transmission using ATM and SCDMA over hybrid fiber coax cable plant
- 33 5,990,955 Dual encoding/compression method and system for picture quality/data density enhancement
- 34 5,984,512 Method for storing video information
- 35 5,978,592 Video decompression and decoding system utilizing control and data tokens
- 36 5,978,379 Fiber channel learning bridge, learning half bridge, and protocol
- 37 5,974,441 WWW client server interactive system method with Java (TM.)
- 38 5,964,835 Storage access validation to data messages using partial storage address data indexed entries containing permissible address range validation for message source
- 39 5,963,154 Technique for decoding variable and fixed length codes
- 40 5,956,741 Interface for connecting a bus to a random access memory using a swing buffer and a buffer manager
- 41 5,956,519 Picture end token in a system comprising a plurality of pipeline stages
- 42 5,951,651 Packet filter system using BITMAP vector of filter rules for routing packet through network
- 43 5,930,254 Non-blocking dynamic fast packet switch for satellite communication system
- 44 5,929,916 Variable bit rate encoding
- 45 5,926,226 Method for adjusting the quality of a video coder

- 46 [5,925,097 Directly programmable distribution element](#)
- 47 [5,924,119 Consistent packet switched memory bus for shared memory multiprocessors](#)
- 48 [5,914,953 Network message routing using routing table information and supplemental enable information for deadlock prevention](#)
- 49 [5,910,949 Packet channel feedback](#)
- 50 [5,909,559 Bus bridge device including data bus of first width for a first processor, memory controller, arbiter circuit and second processor having a different second data width](#)

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Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND rules): 175 patents.

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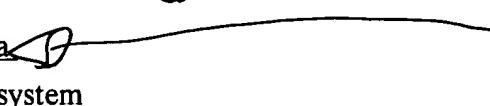
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packet and pipeline and header and rules

PAT. NO. Title

- 51 [5,903,312 Micro-architecture of video core for MPEG-2 decoder](#)
- 52 [5,881,301 Inverse modeller](#)
- 53 [5,878,273 System for microprogrammable state machine in video parser disabling portion of processing stages responsive to sequence sub-- end token generating by token generator responsive to received data](#)
- 54 [5,878,166 Field frame macroblock encoding decision](#)
- 55 [5,877,812 Method and apparatus for increasing channel utilization for digital video transmission](#)
- 56 [5,872,598 Scene change detection using quantization scale factor rate control](#)
- 57 [5,867,652 Method and apparatus for supporting multiple outstanding network requests on a single connection](#)
- 58 [5,867,501 Encoding for communicating data and commands](#)
- 59 [5,864,683 System for providing secure internetwork by connecting type enforcing secure computers to external network for limiting access to data based on user and process access rights](#)
- 60 [5,864,681 Video encoder/decoder system](#)
- 61 [5,854,658 Statistical multiplexing system which encodes a sequence of video images using a plurality of video encoders](#)
- 62 [5,847,761 Method for performing rate control in a video encoder which provides a bit budget for each frame while employing virtual buffers and virtual buffer verifiers](#)
- 63 [5,842,033 Padding apparatus for passing an arbitrary number of bits through a buffer in a pipeline system](#)

- 64 [5,838,894 Logical, fail-functional, dual central processor units formed from three processor units](#)
- 65 [5,835,925 Using external registers to extend memory reference capabilities of a microprocessor](#)
- 66 [5,835,792 Token-based adaptive video processing arrangement](#)
- 67 [5,835,740 Data pipeline system and data encoding method](#)
- 68 [5,832,224 Entity management system](#)
- 69 [5,831,678 Video encoder/decoder system](#)
- 70 [5,829,007 Technique for implementing a swing buffer in a memory array](#)
- 71 [5,828,907 Token-based adaptive video processing arrangement](#)
- 72 [5,822,512 Switching control in a fault tolerant system](#)
- 73 [5,821,885 Video decompression](#)
- 74 [5,818,532 Micro architecture of video core for MPEG-2 decoder](#)
- 75 [5,815,723 Picket autonomy on a SIMD machine](#)
- 76 [5,809,543 Fault tolerant extended processing complex for redundant nonvolatile file caching](#)
- 77 [5,809,270 Inverse quantizer](#)
- 78 [5,805,914 Data pipeline system and data encoding method](#)
- 79 [5,805,228 Video encoder/decoder system](#)
- 80 [5,802,054 Atomic network switch with integrated circuit switch nodes](#)
- 81 [5,801,973 Video decompression](#)
- 82 [5,801,779 Rate control with panic mode](#)
- 83 [5,798,719 Parallel Huffman decoder](#)
- 84 [5,796,715 Non-blocking dynamic fast packet switch for satellite communication system](#)
- 85 [5,793,964 Web browser system](#)
- 86 [5,790,776 Apparatus for detecting divergence between a pair of duplexed, synchronized processor elements](#)
- 87 [5,784,631 Huffman decoder](#)
- 88 [5,771,316 Fade detection](#)
- 89 [5,768,629 Token-based adaptive video processing arrangement](#)
- 90 [5,768,561 Tokens-based adaptive video processing arrangement](#)
- 91 [5,764,293 Method of encoding video using master and slave encoders wherein bit budgets for frames to be encoded are based on encoded frames](#)
- 92 [5,761,663 Method for distributed task fulfillment of web browser requests](#)
- 93 [5,761,398 Three stage hierachal motion vector determination](#)
- 94 [5,752,246 Service agent for fulfilling requests of a web browser](#)
- 95 [5,751,955 Method of synchronizing a pair of central processor units for duplex, lock-step operation by copying data into a corresponding locations of another memory](#)
- 96 [5,751,932 Fail-fast, fail-functional, fault-tolerant multiprocessor system](#)
- 97 [5,745,754 Sub-agent for fulfilling requests of a web browser using an intelligent agent and providing a report](#)

98 ~~5,740,460~~ Arrangement for processing packetized data 

99 5,740,164 Traffic routing for satellite communication system

100 5,724,537 Interface for connecting a bus to a random access memory using a two wire link





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Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND rules): 175 patents.

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PAT. NO. Title

101 [5,724,352](#) Terabit per second packet switch having assignable multiple packet loss probabilities

102 [5,721,908](#) Computer network for WWW server data access over internet

103 [5,717,394](#) Method and apparatus for encoding and decoding data

104 [5,710,918](#) Method for distributed task fulfillment of web browser requests

105 [5,703,793](#) Video decompression

106 [5,701,451](#) Method for fulfilling requests of a web browser

107 [5,689,689](#) Clock circuits for synchronized processor systems having clock generator circuit with a voltage control oscillator producing a clock signal synchronous with a master clock signal

108 [5,686,965](#) Two-part synchronization scheme for digital video decoders

109 [5,686,963](#) Method for performing rate control in a video encoder which provides a bit budget for each frame while employing virtual buffers and virtual buffer verifiers

110 [5,682,204](#) Video encoder which uses intra-coding when an activity level of a current macro-block is smaller than a threshold level

111 [5,680,568](#) Instruction format with sequentially performable operand address extension modification

112 [5,678,007](#) Method and apparatus for supporting multiple outstanding network requests on a single connection

113 [5,675,807](#) Interrupt message delivery identified by storage location of received interrupt data

114 [5,675,579](#) Method for verifying responses to messages using a barrier message

- 115 [5,664,223 System for independently transferring data using two independently controlled DMA engines coupled between a FIFO buffer and two separate buses respectively](#)
- 116 [5,655,112 Method and apparatus for enabling data paths on a remote bus](#)
- 117 [5,650,860 Adaptive quantization](#)
- 118 [5,638,518 Node loop core for implementing transmission protocol in fibre channel](#)
- 119 [5,634,015 Generic high bandwidth adapter providing data communications between diverse communication networks and computer system](#)
- 120 [5,634,004 Directly programmable distribution element](#)
- 121 [5,617,413 Scalable wrap-around shuffle exchange network with deflection routing](#)
- 122 [5,608,907 Extensible entity management system including an information manager for obtaining, storing and retrieving information from entities](#)
- 123 [5,603,012 Start code detector](#)
- 124 [5,598,388 Storing plural data records on tape in an entity with an index entry common to those records](#)
- 125 [5,594,869 Method and apparatus for end-to-end encryption of a data packet in a computer network](#)
- 126 [5,592,622 Network intermediate system with message passing architecture](#)
- 127 [5,592,556 Digital radio with vocoding encrypting codec](#)
- 128 [5,574,849 Synchronized data transmission between elements of a processing system](#)
- 129 [5,561,669 Computer network switching system with expandable number of ports](#)
- 130 [5,557,796 Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands](#)
- 131 [5,553,095 Method and apparatus for exchanging different classes of data during different time intervals](#)
- 132 [5,537,549 Communication network with time coordinated station activity by time slot and periodic interval number](#)
- 133 [5,530,695 UPC-based traffic control framework for ATM networks](#)
- 134 [5,511,165 Method and apparatus for communicating data across a bus bridge upon request](#)
- 135 [5,493,571 Apparatus and method for digital communications with improved delimiter detection](#)
- 136 [5,475,838 Extensible entity management system including rule-based alarms](#)
- 137 [5,471,488 Clock fault detection circuit](#)
- 138 [5,467,342 Methods and apparatus for time stamp correction in an asynchronous transfer mode network](#)
- 139 [5,455,825 Tag-based scheduling system for digital communication switch](#)
- 140 [5,452,006 Two-part synchronization scheme for digital video decoders](#)
- 141 [5,448,558 Method and apparatus for managing packet FIFOs](#)
- 142 [5,440,698 Arbitration of packet switched busses, including busses for shared memory multiprocessors](#)
- 143 [5,426,727 High-quality character generating system and method for use therein](#)
- 144 [5,400,331 Communication network interface with screeners for incoming messages](#)

145 [5,392,396 Method and apparatus for gradually degrading video data](#)

146 [5,367,643 Generic high bandwidth adapter having data packet memory configured in three level hierarchy for temporary storage of variable length data packets](#)

147 [5,345,587 Extensible entity management system including a dispatching kernel and modules which independently interpret and execute commands](#)

148 [5,327,428 Collision-free insertion and removal of circuit-switched channels in a packet-switched transmission structure](#)

149 [5,321,813 Reconfigurable, fault tolerant, multistage interconnect network and protocol](#)

150 [5,311,509 Configurable gigabits switch adapter](#)





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Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND rules): 175 patents.

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packet and pipeline and header and rules

PAT. NO. Title

151 [5,298,895 Data compression method and apparatus utilizing an adaptive dictionary](#)

152 [5,280,600 Storage of compressed data with algorithm](#)

153 [5,265,235 Consistency protocols for shared memory multiprocessors](#)

154 [5,251,322 Method of operating a computer graphics system including asynchronously traversing its nodes](#)

155 [5,235,644 Probabilistic cryptographic processing method](#)

156 [5,228,083 Cryptographic processing in a communication network, using a single cryptographic engine](#)

157 [5,201,039 Multiple address-space data processor with addressable register and context switching](#)

158 [5,182,811 Exception, interrupt, and trap handling apparatus which fetches addressing and context data using a single instruction following an interrupt](#)

159 [5,161,193 Pipelined cryptography processor and method for its use in communication networks](#)

160 [5,155,822 High performance graphics workstation](#)

161 [5,099,517 Frame status encoding for communication networks](#)

162 [5,097,411 Graphics workstation for creating graphics data structure which are stored retrieved and displayed by a graphics subsystem for competing programs](#)

163 [5,086,469 Encryption with selective disclosure of protocol identifiers](#)

164 [5,070,528 Generic encryption technique for communication networks](#)

165 [5,029,069 Data processor](#)

166 [5,008,882 Method and apparatus for eliminating unsuccessful tries in a search tree](#)

167 [4,945,500 Triangle processor for 3-D graphics display system](#)

168 [4,928,247 Method and apparatus for the continuous and asynchronous traversal and processing of](#)

168 [4,928,247 Method and apparatus for the continuous and asynchronous traversal and processing of graphics data structures](#)

169 [4,881,230 Expert system for processing errors in a multiplex communications system](#)

170 [4,873,687 Failing resource manager in a multiplex communication system](#)

171 [4,817,092 Threshold alarms for processing errors in a multiplex communications system](#)

172 [4,814,980 Concurrent hypercube system with improved message passing](#)

173 [4,814,978 Dataflow processing element, multiprocessor, and processes](#)

174 [4,734,848 Combination reduction processing method and apparatus](#)

175 [4,438,494 Apparatus of fault-handling in a multiprocessing system](#)

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Results of Search in 1976-2000 db for:

((packet AND pipeline) AND header) AND rules): 175 patents.

Hits 151 through 175 out of 175

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PAT. NO. Title

151 [5,298,895 Data compression method and apparatus utilizing an adaptive dictionary](#)

152 [5,280,600 Storage of compressed data with algorithm](#)

153 [5,265,235 Consistency protocols for shared memory multiprocessors](#)

154 [5,251,322 Method of operating a computer graphics system including asynchronously traversing its nodes](#)

155 [5,235,644 Probabilistic cryptographic processing method](#)

156 [5,228,083 Cryptographic processing in a communication network, using a single cryptographic engine](#)

157 [5,201,039 Multiple address-space data processor with addressable register and context switching](#)

158 [5,182,811 Exception, interrupt, and trap handling apparatus which fetches addressing and context data using a single instruction following an interrupt](#)

159 [5,161,193 Pipelined cryptography processor and method for its use in communication networks](#)

160 [5,155,822 High performance graphics workstation](#)

161 [5,099,517 Frame status encoding for communication networks](#)

162 [5,097,411 Graphics workstation for creating graphics data structure which are stored retrieved and displayed by a graphics subsystem for competing programs](#)

163 [5,086,469 Encryption with selective disclosure of protocol identifiers](#)

164 [5,070,528 Generic encryption technique for communication networks](#)

165 [5,029,069 Data processor](#)

166 [5,008,882 Method and apparatus for eliminating unsuccessful tries in a search tree](#)

167 [4,945,500 Triangle processor for 3-D graphics display system](#)

168 [4,928,247 Method and apparatus for the continuous and asynchronous traversal and processing of](#)

168 4,928,247 [Method and apparatus for the continuous and asynchronous traversal and processing of graphics data structures](#)

169 4,881,230 [Expert system for processing errors in a multiplex communications system](#)

170 4,873,687 [Failing resource manager in a multiplex communication system](#)

171 4,817,092 [Threshold alarms for processing errors in a multiplex communications system](#)

172 4,814,980 [Concurrent hypercube system with improved message passing](#)

173 4,814,978 [Dataflow processing element, multiprocessor, and processes](#)

174 4,734,848 [Combination reduction processing method and apparatus](#)

175 4,438,494 [Apparatus of fault-handling in a multiprocessing system](#)

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((packet AND pipeline) AND header) AND bitmap: 27 patents.

Hits 1 through 27 out of 27

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packet and pipeline and header and bitmap

PAT. NO. Title

- 1 [6,125,114](#) Switching system comprising distributed elements allowing attachment to line adapters, and having multicasting capabilities
- 2 [6,122,275](#) Real-time processing for virtual circuits in packet switching
- 3 [6,118,724](#) Memory controller architecture
- 4 [6,108,334](#) Switching system comprising distributed elements allowing attachment to line adapters
- 5 [6,094,655](#) Method of creating and using notes decision capsules
- 6 [6,091,857](#) System for producing a quantized signal
- 7 [6,081,665](#) Method for efficient soft real-time execution of portable byte code computer programs
- 8 [6,061,749](#) Transformation of a first dataword received from a FIFO into an input register and subsequent dataword from the FIFO into a normalized output dataword
- 9 [6,055,235](#) Switching system
- 10 [5,974,441](#) WWW client server interactive system method with Java (.TM.)
- 11 [5,951,651](#) Packet filter system using BITMAP vector of filter rules for routing packet through network
- 12 [5,883,901](#) Communications system including synchronization information for timing upstream transmission of data and ability to vary slot duration
- 13 [5,793,964](#) Web browser system
- 14 [5,787,483](#) High-speed data communications modem
- 15 [5,784,597](#) Communications network system including acknowledgement indicating successful receipt of request for reserved communication slots and start time for said reserved communication slots
- 16 [5,761,663](#) Method for distributed task fulfillment of web browser requests
- 17 [5,752,246](#) Service agent for fulfilling requests of a web browser
- 18 [5,745,754](#) Sub-agent for fulfilling requests of a web browser using an intelligent agent and

- 18 [5,745,754](#) Sub-agent for fulfilling requests of a web browser using an intelligent agent and providing a report
- 19 [5,721,908](#) Computer network for WWW server data access over internet
- 20 [5,717,394](#) Method and apparatus for encoding and decoding data
- 21 [5,710,918](#) Method for distributed task fulfillment of web browser requests
- 22 [5,701,451](#) Method for fulfilling requests of a web browser
- 23 [5,611,038](#) Audio/video transceiver provided with a device for reconfiguration of incompatibly received or transmitted video and audio information
- 24 [5,251,322](#) Method of operating a computer graphics system including asynchronously traversing its nodes
- 25 [5,155,822](#) High performance graphics workstation
- 26 [5,097,411](#) Graphics workstation for creating graphics data structure which are stored retrieved and displayed by a graphics subsystem for competing programs
- 27 [4,928,247](#) Method and apparatus for the continuous and asynchronous traversal and processing of graphics data structures

